

HFC - 4S / HFC - 8S

Evaluation Board V 2.1

Switches, Connectors and Jumper

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List of Switches:	Code	Description
	SW100	100 Ω termination of the 1 st S/T interface
	SW101	TE/NT mode switch of the 1 st S/T interface
	SW200	100 Ω termination of the 2 nd S/T interface
	SW201	TE/NT mode switch of the 2 nd S/T interface
	SW300	100 Ω termination of the 3 rd S/T interface
	SW301	TE/NT mode switch of the 3 rd S/T interface
	SW400	100 Ω termination of the 4 th S/T interface
	SW401	TE/NT mode switch of the 4 th S/T interface

- Lines are terminated with 100 Ω in switch position 'ON'.
- An S/T interface is configured to be in TE mode with the switch slide near to the termination switch. When the slide is near to the ISDN jack, NT mode configuration is selected.

List of Connectors:	Code	Description
	CON1	SPI connector
	CON2	PCM port
	CON3	5 th and 6 th S/T interface / GPIO[11..8], GPIO[23..16]
	CON4	7 th and 8 th S/T interface / GPIO[15..12], GPIO[31..24]
	CON5	3.3 V measurement and test lead
	CON6	GND measurement and test lead
	CON7	5.0 V measurement and test lead
	CON8	Connector for PCM time slot count / PCM CODEC enable
	CON100	RJ45 connector of the 1 st S/T interface
	CON101	NT power feeding of the 1 st S/T interface
	CON200	RJ45 connector of the 2 nd S/T interface
	CON201	NT power feeding of the 2 nd S/T interface
	CON300	RJ45 connector of the 3 rd S/T interface
	CON301	NT power feeding of the 3 rd S/T interface
	CON400	RJ45 connector of the 4 th S/T interface
	CON401	NT power feeding of the 4 th S/T interface

List of Jumpers:	Code	Description
	JP1	EE_SCL/EN to GND, disables EEPROM if connected
	JP2	3.3 V power supply from PCI interface or voltage regulator

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